

Description

The AP7366 is a 500mA, adjustable and fixed output voltage, low dropout linear regulator. This device includes pass element, error amplifier, band-gap, current limit and thermal shutdown circuitry. The device is turned on when EN pin is set to logic high level.

The characteristics of low dropout voltage and low quiescent current make it suitable for low power applications such as battery powered devices. The typical quiescent current is approximately 60 μ A. Built-in current-limit and thermal-shutdown functions prevent IC from damage in fault conditions.

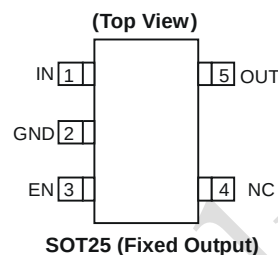
This device is available fixed version with 1.8V and 2.5V outputs. Please contact your local sales office for other voltage options.

The AP7366 is available in SOT25 package.

Features

- 500mA Low Dropout Regulator with EN
- Low I_Q : 60 μ A
- Wide Input Voltage Range: 2.2V to 6V
- Fixed Output Options: 1.8V and 2.5V
- High PSRR: 75dB at 1kHz
- Fast Start-Up Time: 150 μ s
- Stable with Low ESR, 1 μ F Ceramic Output Capacitor
- Excellent Load/Line Transient Response
- Low Dropout: 300mV at 500mA
- Current Limit and Short Circuit Protection
- Thermal Shutdown Protection
- Ambient Temperature Range: -40°C to +85°C
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. "Green" Device (Note 3)**

Pin Assignments

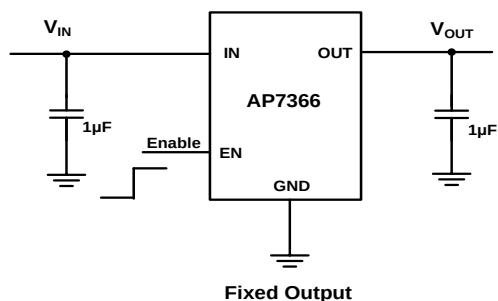


Applications

- Servers and Notebook Computers
- Low and Medium Power Applications
- FPGA and DSP Core or I/O Power
- Consumer Electronics

- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.

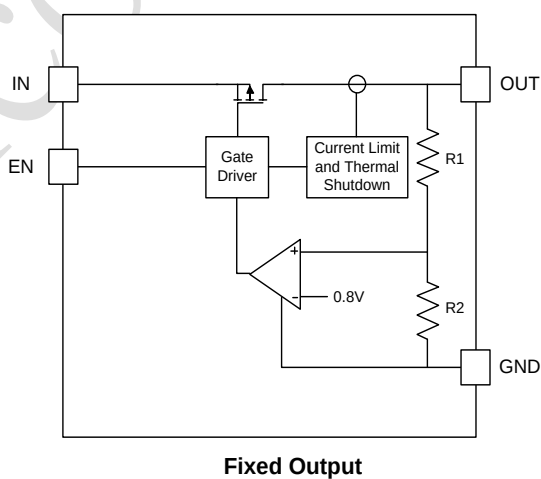
Typical Applications Circuit



Pin Descriptions

Pin Name	Pin Number	Functions
	SOT25 (Fixed Output)	
IN	1	Voltage Input Pin. Bypass to ground through at least 1µF MLCC capacitor
GND	2	Ground
EN	3	Enable Input, Active High
ADJ	—	Output Feedback Pin
NC	4	No Connection
OUT	5	Voltage Output Pin. Bypass to ground through 1µF MLCC capacitor

Functional Block Diagram



Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Ratings	Unit
ESD HBM	Human Body Model ESD Protection	2000	V
ESD CDM	Charge Device Model	±1000	V
V _{IN}	Input Voltage	6.5	V
—	OUT, EN Voltage	V _{IN} +0.3	V
T _{ST}	Storage Temperature Range	-65 to +150	°C
T _J	Maximum Junction Temperature	+150	°C

Recommended Operating Conditions (@T_A = +25°C, unless otherwise specified.)

Symbol	Parameter	Min	Max	Unit
V _{IN}	Input Voltage	2.2	6	V
V _{OUT}	Output Voltage	0.8	5	V
I _{OUT}	Output Current (Note 4)	0	500	mA
T _A	Operating Ambient Temperature	-40	+85	°C

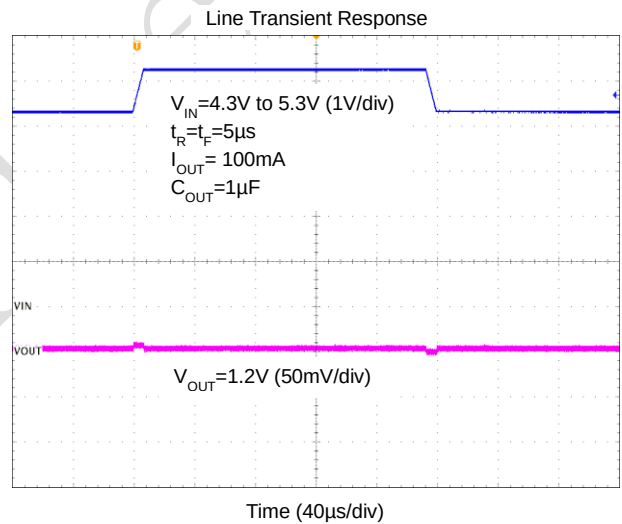
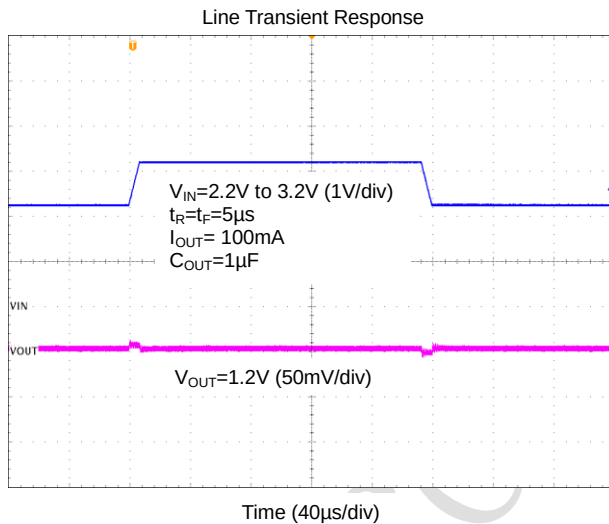
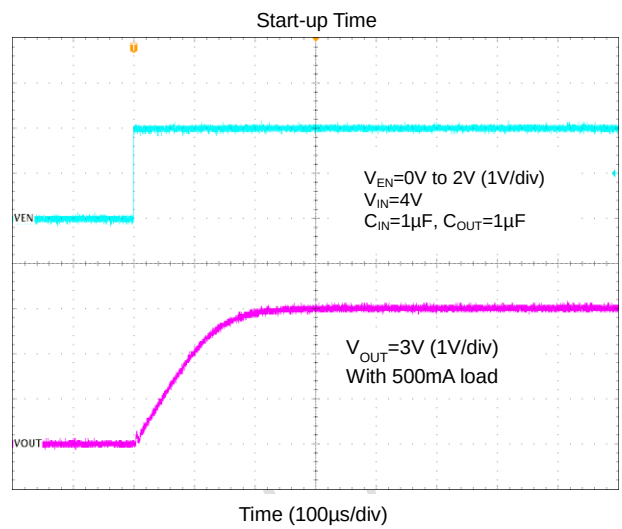
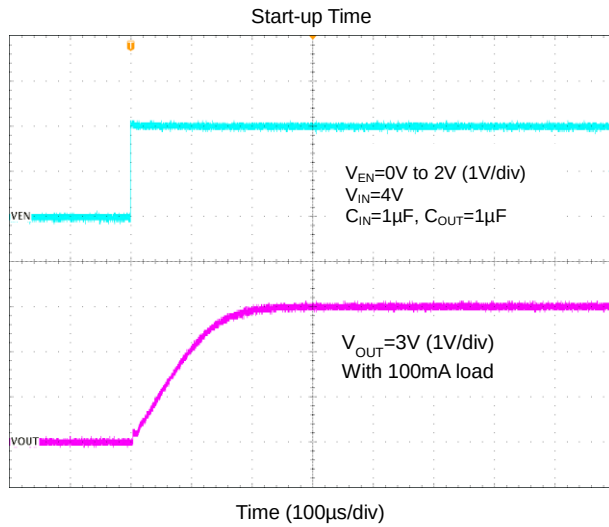
Note: 4. The device maintains a stable, regulated output voltage without a load current.

Electrical Characteristics (@T_A = +25°C, V_{IN} = V_{OUT} + 1V, C_{OUT} = 1μF, C_{IN} = 1μF, V_{EN} = V_{IN}, unless otherwise specified.)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
I _Q	Input Quiescent Current	V _{EN} = V _{IN} , I _{OUT} = 0mA	—	60	80	μA
V _{OUT}	Output Voltage Accuracy	I _{OUT} = 10mA	1.0V ≤ V _{OUT} < 1.5V	V _{OUT} - 0.015	V _{OUT}	V
			1.5V ≤ V _{OUT} ≤ 3.9V	V _{OUT} * 0.99	V _{OUT} * 1.01	%
I _{SHDN}	Input Shutdown Current	V _{EN} = 0V, I _{OUT} = 0mA	-1.0	0.05	1.0	μA
ΔV _{OUT} / ΔV _{IN} / V _{OUT}	Line Regulation	V _{IN} = (V _{OUT} + 1V) to 5.5V, I _{OUT} = 10mA	T _A = +25°C	—	0.01	0.1
			-40°C ≤ T _A ≤ +85°C	—	—	0.2
ΔV _{OUT} / V _{OUT}	Load Regulation	I _{OUT} = 1mA to 600mA	1.2V < V _{OUT} ≤ 3.9V	-1.0	—	+1.0
			1.0V ≤ V _{OUT} ≤ 1.2V	-1.5	—	+1.5
V _{DROPOUT}	Dropout Voltage (Note 5)	V _{OUT} = 1.8V, I _{OUT} = 300mA	—	160	250	mV
		V _{OUT} = 2.5V, I _{OUT} = 300mA	—	125	190	
V _{DROPOUT}	Dropout Voltage (Note 5)	V _{OUT} = 1.8V, I _{OUT} = 500mA	—	320	420	mV
		V _{OUT} = 2.5V, I _{OUT} = 500mA	—	250	380	
PSRR	PSRR (Note 6)	f = 1kHz, I _{OUT} = 100mA	—	75	—	dB
		f = 10kHz, I _{OUT} = 100mA	—	55	—	dB
I _{SHORT}	Short-Circuit Current	V _{IN} = V _{OUT} + 1V, Output Voltage < 15% of V _{OUT}	—	250	—	mA
t _{ST}	Start-Up Time	V _{OUT} = 0V to 3.0V, R _L = 30Ω	—	150	—	μs
I _{LIMIT}	Current Limit	V _{IN} = V _{OUT} + 1V	0.66	1.0	—	A
V _{IL}	EN Input Logic Low Voltage	V _{IN} = V _{IN-Min} to V _{IN-Max}	—	—	0.3	V
V _{IH}	EN Input Logic High Voltage	V _{IN} = V _{IN-Min} to V _{IN-Max}	1.0	—	V _{IN}	V
I _{EN}	EN Input Leakage Current	V _{IN} = 5.5V or V _{EN} = 0V	-0.1	—	+0.1	μA
T _{SHDN}	Thermal Shutdown Threshold	—	—	+150	—	°C
T _{HYS}	Thermal Shutdown Hysteresis	—	—	+20	—	°C
R _{ENPD}	EN Pull-Down Resistor	—	—	3	—	MΩ
R _{PD}	Output Discharge Resistor	V _{OL} = 1V	—	100	—	Ω
ΔV _{OUT} / ΔT _A / V _{OUT}	Output Voltage Temperature Coefficient	I _{OUT} = 100mA, -40°C ≤ T _A ≤ +85°C	—	±100	—	ppm/°C
θ _{JA}	Thermal Resistance Junction-to-Ambient	SOT25 (Note 7)	—	169	—	°C/W
			—	132	—	
θ _{JC}	Thermal Resistance Junction-to-Case	SOT25 (Note 7)	—	31	—	°C/W
			—	48	—	

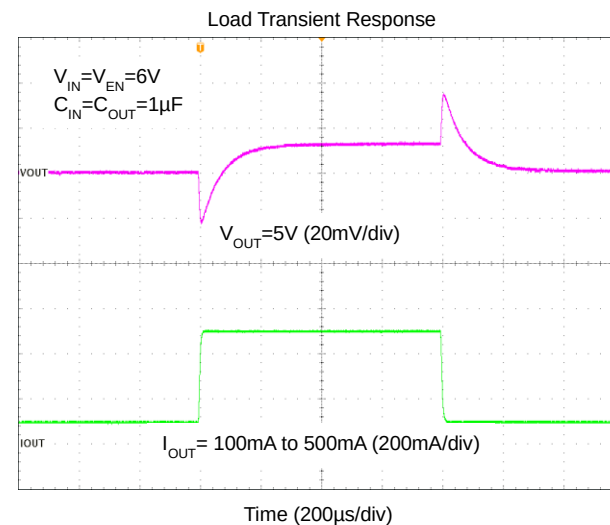
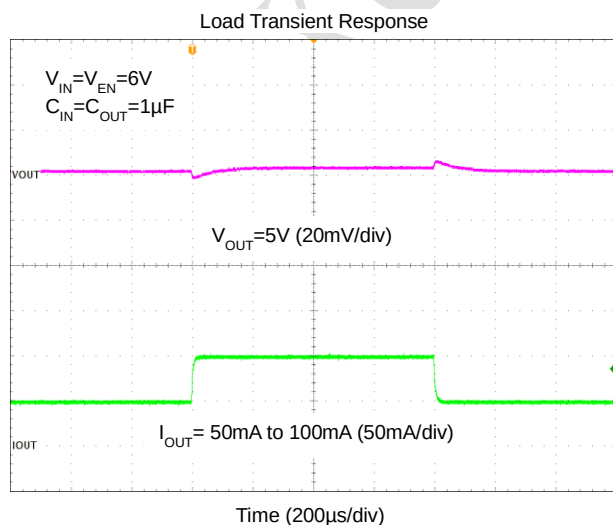
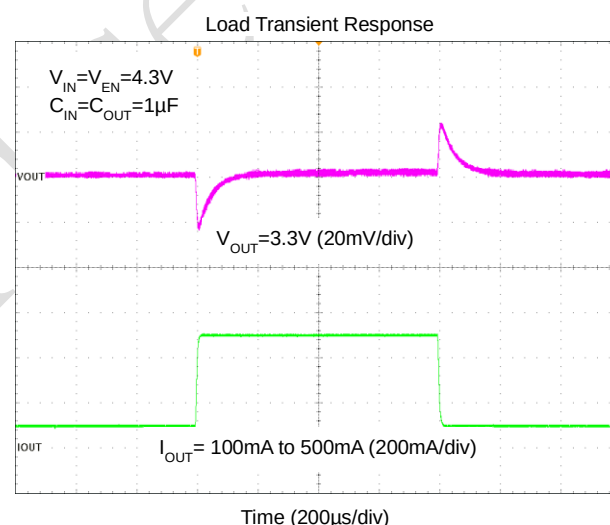
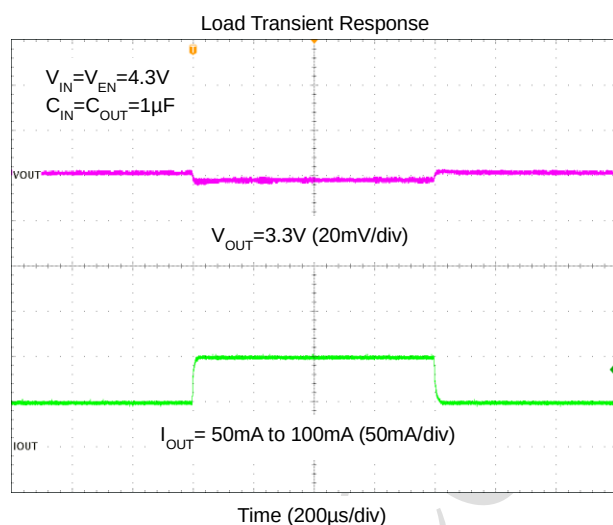
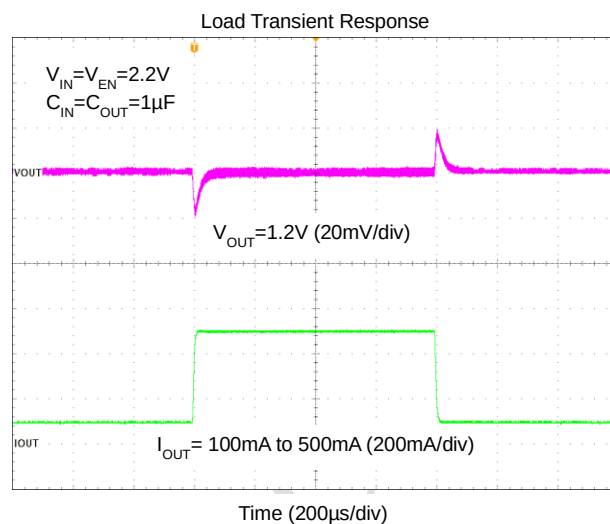
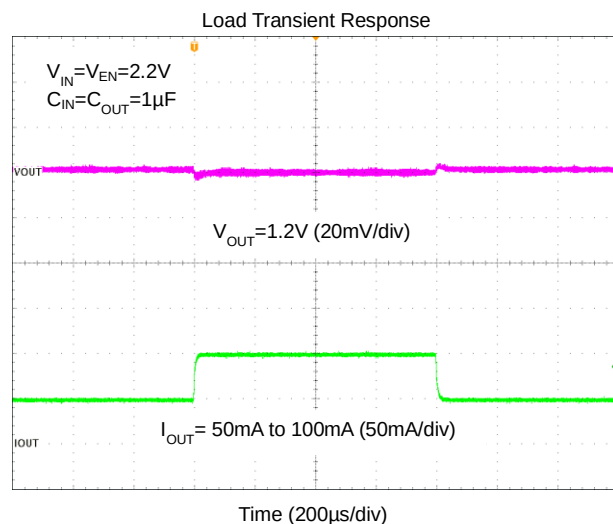
- Notes:
- Dropout voltage is the voltage difference between the input and the output at which the output voltage drops 2% below its nominal value. This parameter only applies to input voltages above minimum V_{IN} = 2.0V.
 - At V_{IN} < 2.3V, the PSRR performance may be reduced.
 - Test condition for all packages: Device mounted on FR-4 substrate PC board, 1oz copper, with minimum recommended pad layout.

Typical Performance Characteristics



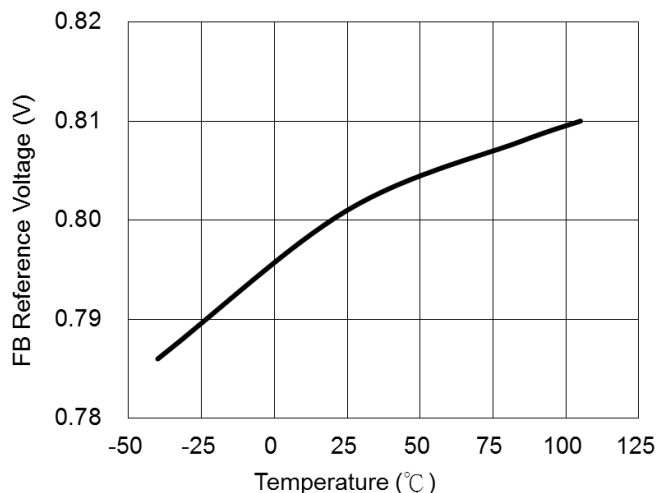
Typical Performance Characteristics (Continued)

NEW PRODUCT

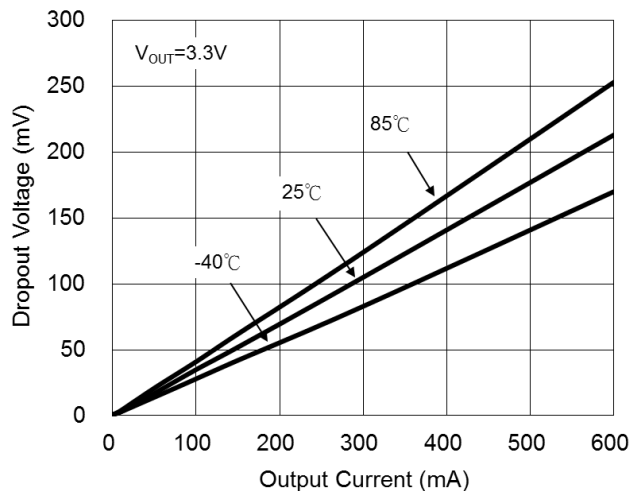


Typical Performance Characteristics (Continued)

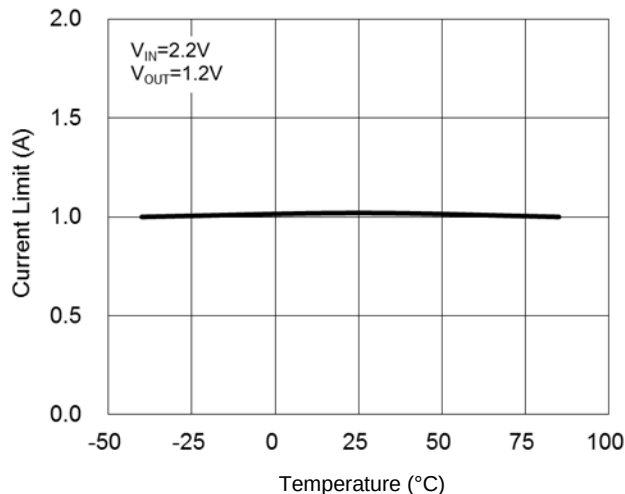
FB Reference Voltage vs. Temperature



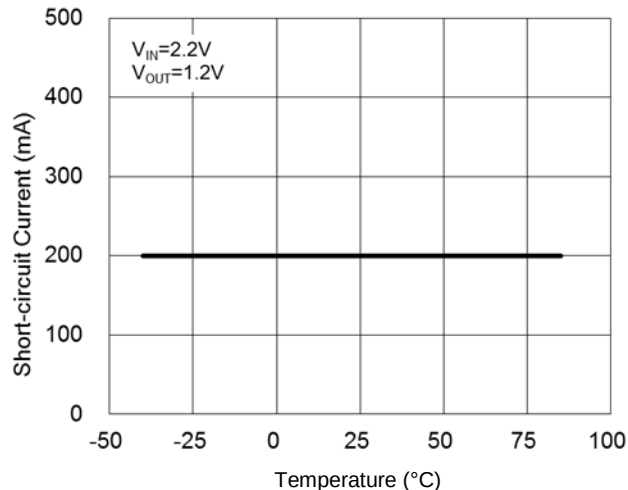
Dropout Voltage vs. Output Current



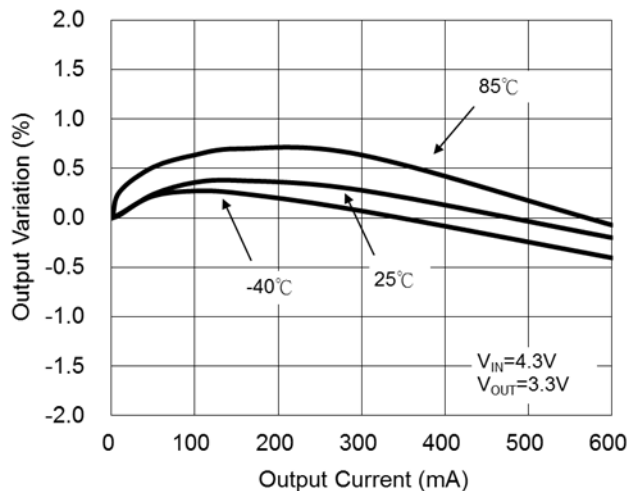
Current Limit vs. Temperature



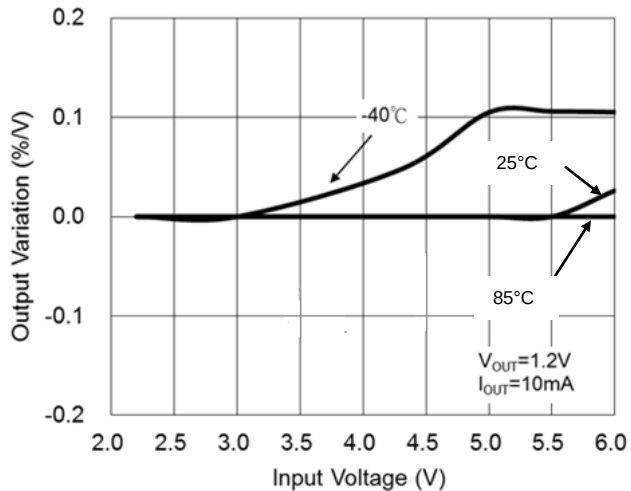
Short-circuit Current vs. Temperature



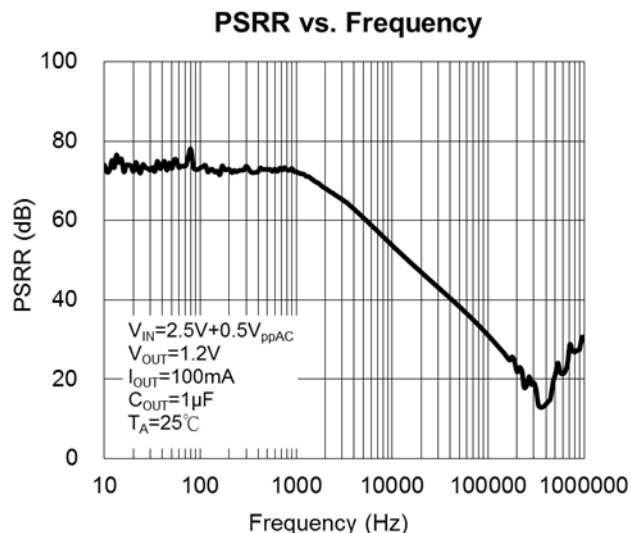
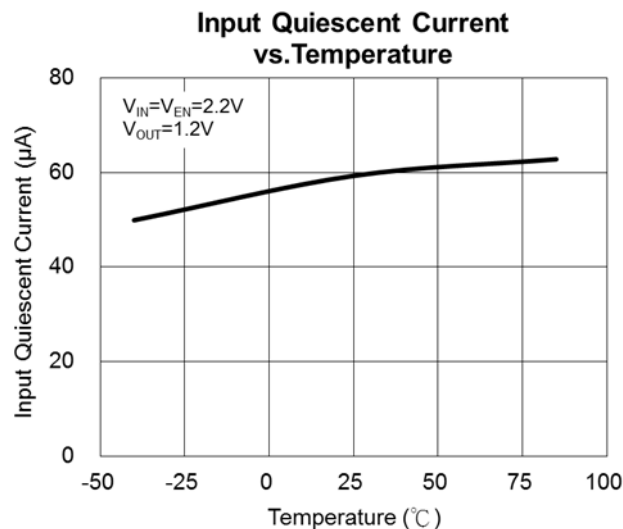
Load Regulation



Line Regulation



Typical Performance Characteristics (Continued)



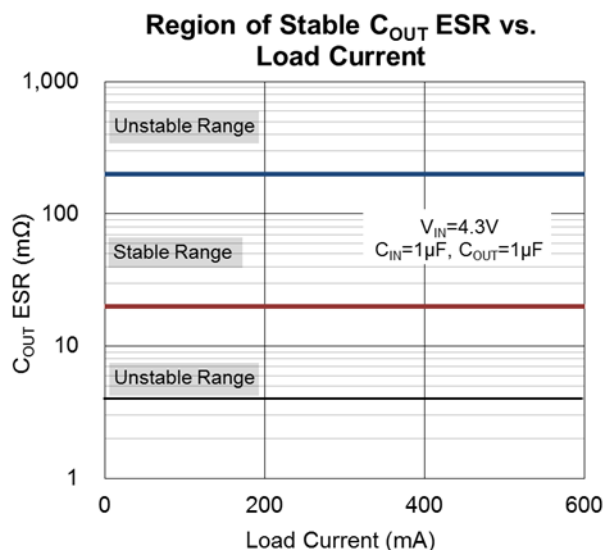
Application Information

Input Capacitor

A $1\mu\text{F}$ ceramic capacitor is recommended between IN and GND pins to decouple input power supply glitch and noise. The amount of the capacitance may be increased without limit. This input capacitor must be located as close as possible to the device to assure input stability and reduce noise. For PCB layout, a wide copper trace is required for both IN and GND pins. A lower ESR capacitor type allows the use of less capacitance, while higher ESR type requires more capacitance.

Output Capacitor

The output capacitor is required to stabilize and improve the transient response of the LDO. The AP7366 is stable with very small ceramic output capacitors. Using a ceramic capacitor value that is at least $2.2\mu\text{F}$ with $10\text{m}\Omega \leq \text{ESR} \leq 300\text{m}\Omega$ on the output ensures stability. Higher capacitance values help to improve line and load transient response. The output capacitance may be increased to keep low undershoot and overshoot. Output capacitor must be placed as close as possible to OUT and GND pins.



Application Information (Continued)

No Load Stability

Other than external resistor divider, no minimum load is required to keep the device stable. The device will remain stable and regulated in no load condition.

ON/OFF Input Operation

The AP7366 is turned on by setting the EN pin high, and is turned off by pulling it low. If this feature is not used, the EN pin should be tied to IN pin to keep the regulator output on at all time. To ensure proper operation, the signal source used to drive the EN pin must be able to swing above and below the specified turn-on/off voltage thresholds listed in the Electrical Characteristics section under V_{IL} and V_{IH} .

Current Limit Protection

When output current at OUT pin is higher than current limit threshold, the current limit protection will be triggered and clamp the output current to prevent over-current and to protect the regulator from damage due to overheating.

Short Circuit Protection

When OUT pin is short-circuit to GND, short circuit protection will be triggered and clamp the output current to approximately 250mA. Full current is restored when the output voltage exceeds 15% of V_{OUT} . This feature protects the regulator from over-current and damage due to overheating.

Thermal Shutdown Protection

Thermal protection disables the output when the junction temperature rises to approximately +150°C, allowing the device to cool down. When the junction temperature reduces to approximately +130°C, the output circuitry is enabled again. Depending on power dissipation, thermal resistance, and ambient temperature, the thermal protection circuit may cycle on and off. This cycling limits the heat dissipation of the regulator, protecting it from damage due to overheating.

Ultra Fast Start-up

After enabled, the AP7366 is able to provide full power in as little as tens of microseconds, typically 200μs, without sacrificing low ground current. This feature will help load circuitry move in and out of standby mode in real time, eventually extend battery life for mobile phones and other portable devices.

Low Quiescent Current

The AP7366, consuming only around 60μA for all input range, provides great power saving in portable and low power applications.

Power Dissipation

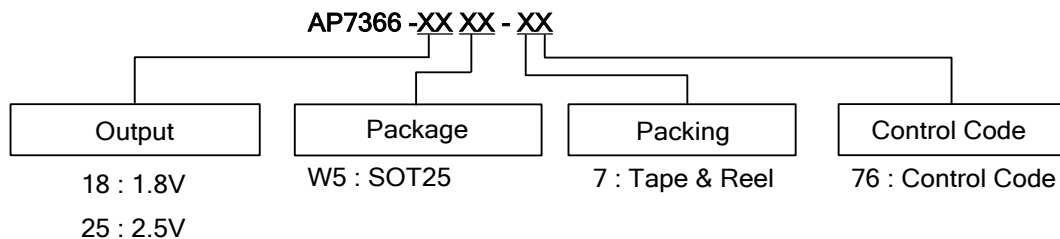
The device power dissipation and proper sizing of the thermal plane that is connected to the thermal pad is critical to avoid thermal shutdown and ensure reliable operation. Power dissipation of the device depends on input voltage and load conditions and can be calculated by:

$$P_D = (V_{IN} - V_{OUT}) \times I_{OUT}$$

The maximum power dissipation, handled by the device, depends on the maximum junction to ambient thermal resistance, maximum ambient temperature, and maximum device junction temperature, which can be calculated by the equation in the following.

$$P_D(\text{max}@T_A) = \frac{(+150^\circ\text{C} - T_A)}{R_{\theta JA}}$$

Ordering Information

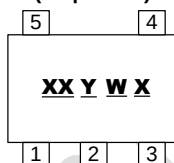


Part Number	Package Code	Packaging	7" Tape and Reel		Control Code
			Quantity	Part Number Suffix	
AP7366-XXW5-7-76	W5	SOT25	3,000/Tape & Reel	-7	76

Marking Information

(1) SOT25

(Top View)



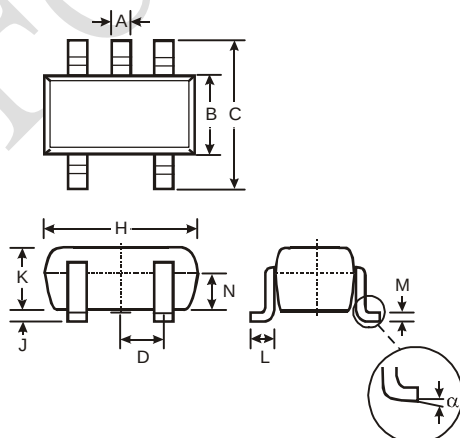
XX : Identification code
 Y : Year 0~9
 W : Week : A~Z : 1~26 week;
 a~z : 27~52 week; z represents
 52 and 53 week
 X : Internal code

Part Number	Package	Identification Code
AP7366-18W5-7	SOT25	Y7
AP7366-25W5-7	SOT25	Y9

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

(1) Package Type: SOT25

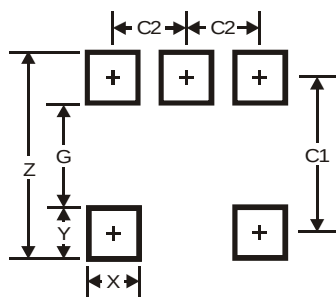


SOT25			
Dim	Min	Max	Typ
A	0.35	0.50	0.38
B	1.50	1.70	1.60
C	2.70	3.00	2.80
D	-	-	0.95
H	2.90	3.10	3.00
J	0.013	0.10	0.05
K	1.00	1.30	1.10
L	0.35	0.55	0.40
M	0.10	0.20	0.15
N	0.70	0.80	0.75
α	0°	8°	-
All Dimensions in mm			

Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

(1) Package Type: SOT25



Dimensions	Value
Z	3.20
G	1.60
X	0.55
Y	0.80
C1	2.40
C2	0.95

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